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(71) Applicant: **MURATA MFG CO LTD**

(72) Inventor:
KURODA TAKAKAZU
TANIGUCHI MASAOKI
NAITO YASUYUKI
HORI HARUO
KONDO TAKANORI

(54) **LAYERED CAPACITOR, WIRING BOARD AND
HIGH FREQUENCY CIRCUIT**

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(57) Abstract:

PROBLEM TO BE SOLVED: To reduce the serial equivalent inductance (ESL) of a layered capacitor.

SOLUTION: First and second side face terminal electrodes 12 and 13 are alternately arrayed on the four sides 4-7 of a capacitor main body 8, and first and second main surface terminal electrodes 14 and 15 are arranged on the main surface 2. First and second internal electrodes facing each other provided inside the capacitor main body 8 are respectively electrically connected to the first and second side terminal electrodes 12 and 13 at respective edges and electrically connected to the first and second main surface terminal electrodes 14 and 15 through via hole conductors. In such a manner, a current flowing in this capacitor 1 is turned to various directions, a current length is shortened and thus the ESL is reduced.

